

FIG. 1

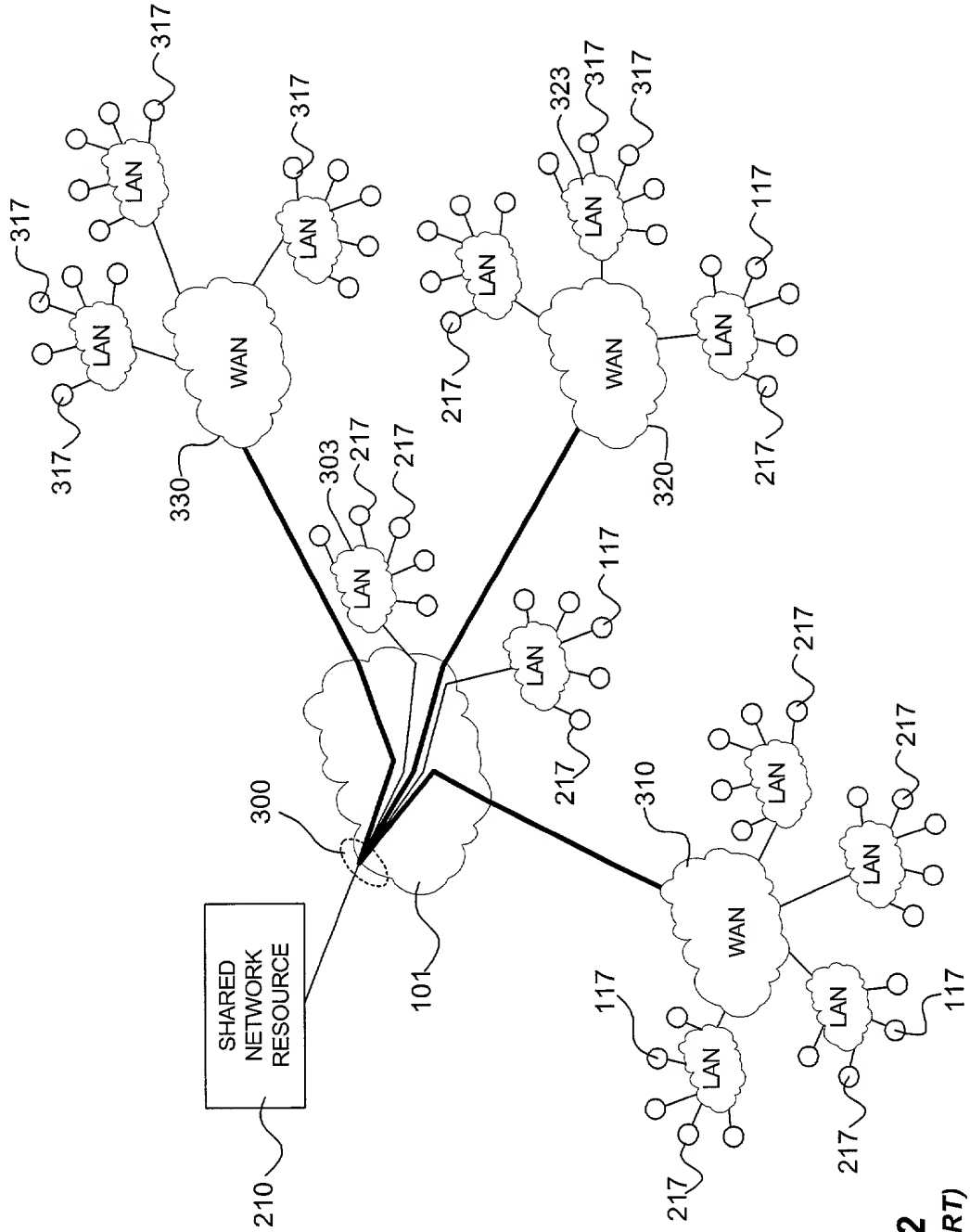
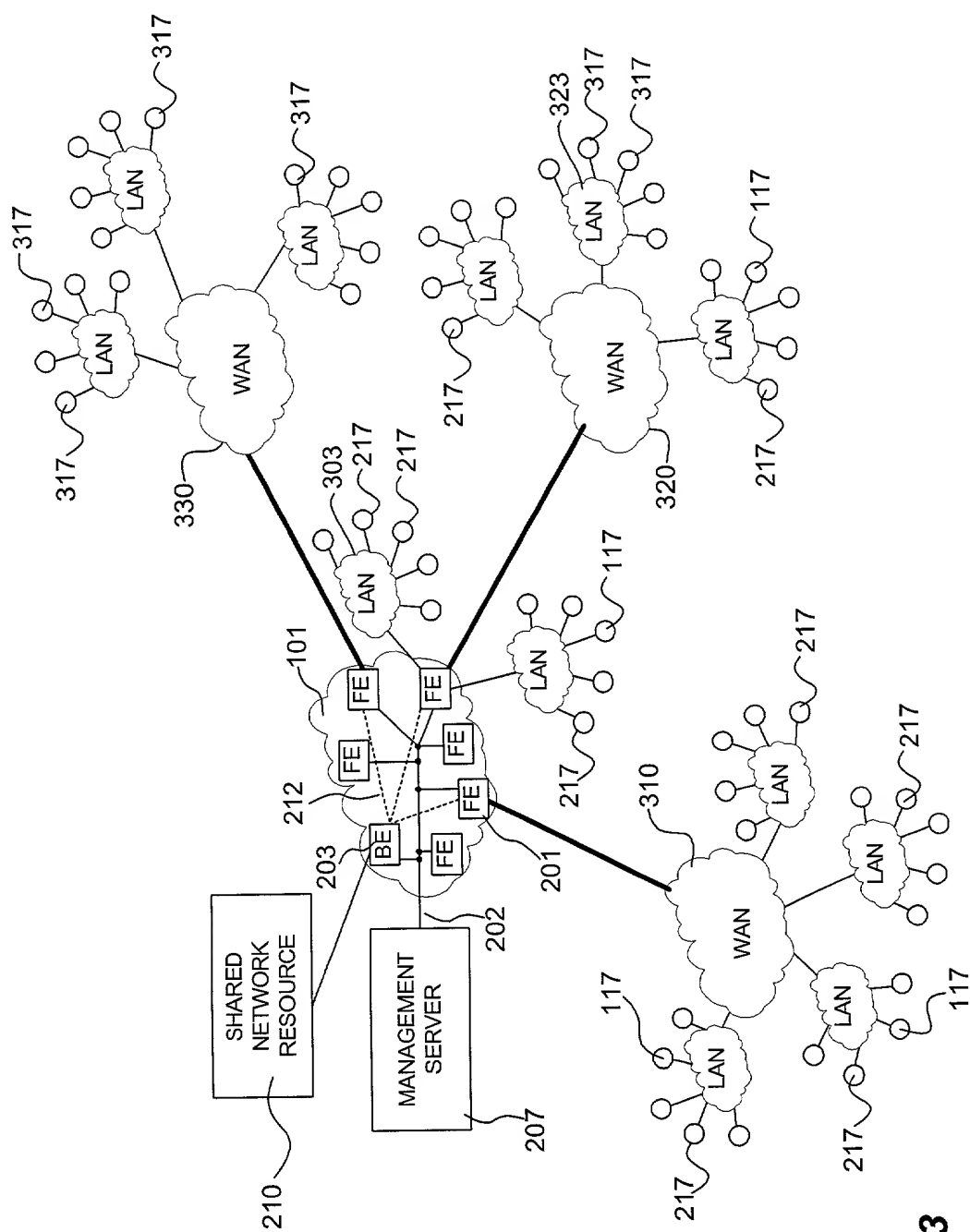


FIG. 2
(PRIOR ART)



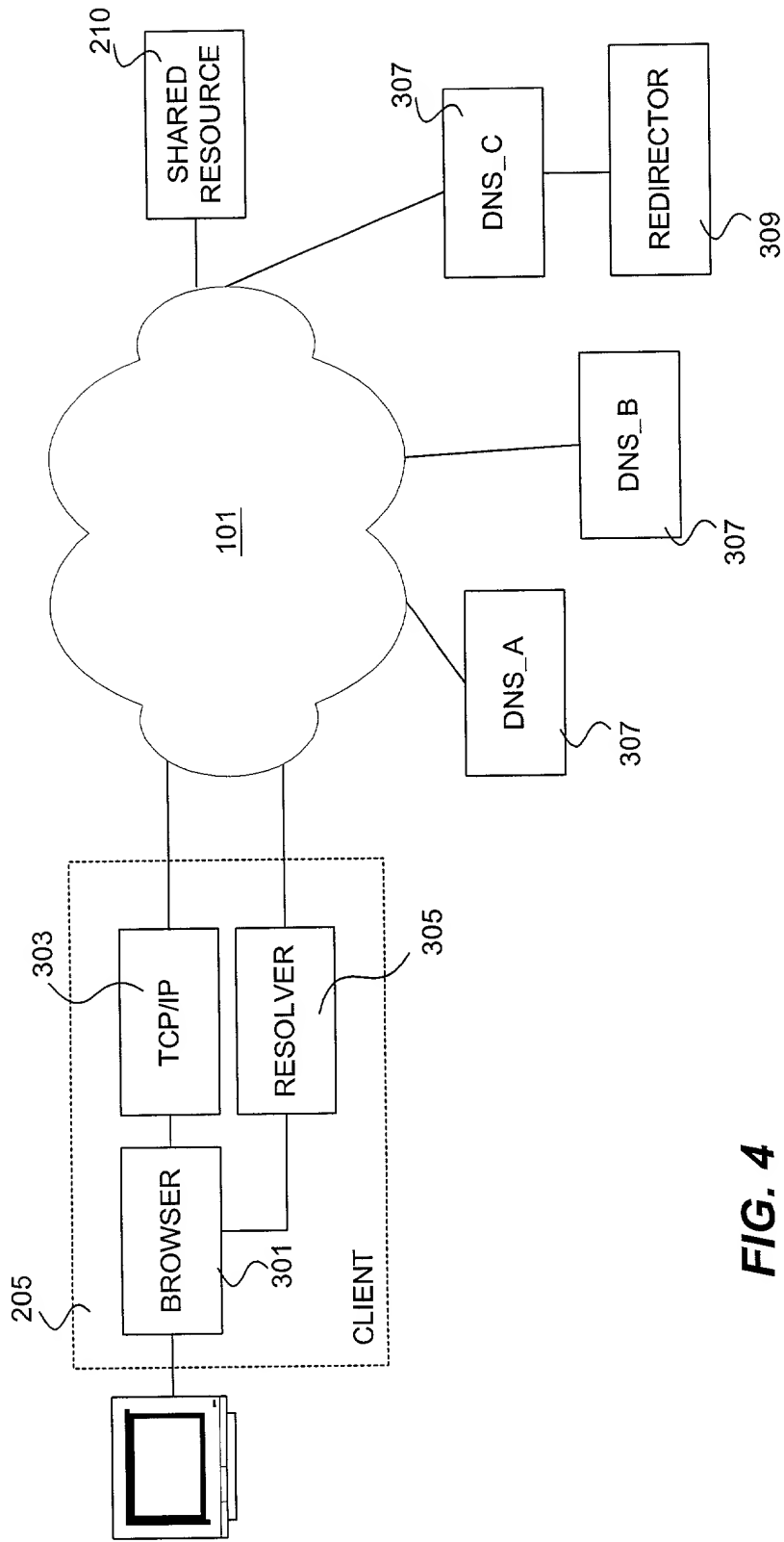


FIG. 4

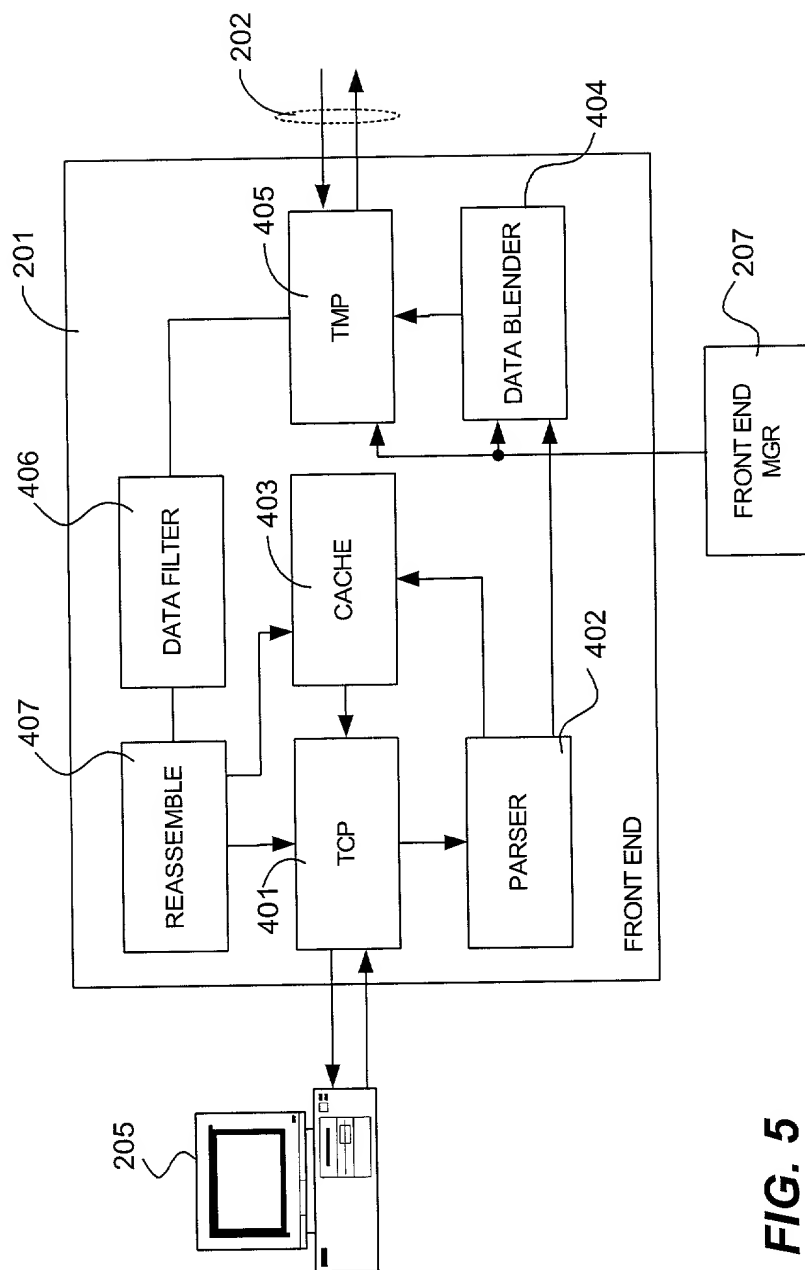


FIG. 5

FIG. 6 is a block diagram of a system architecture for data processing.

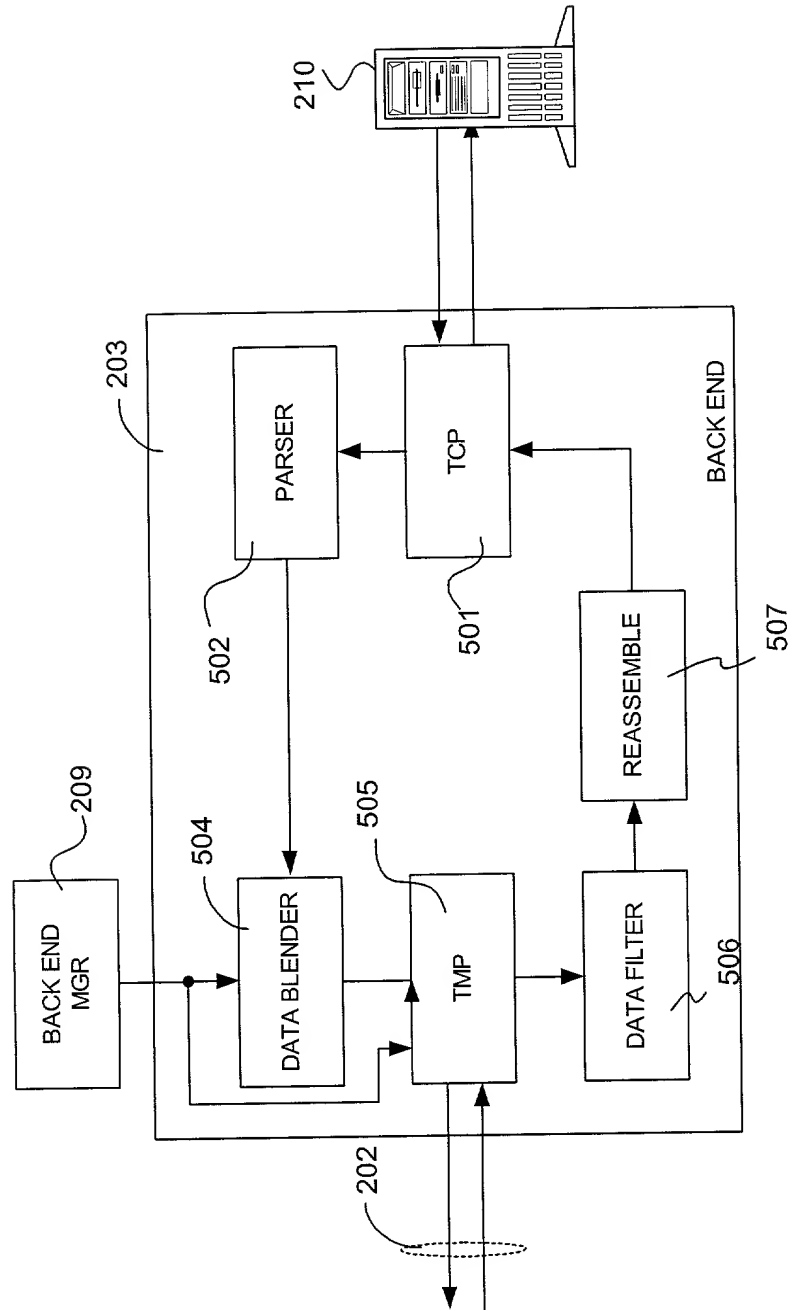


FIG. 6